

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  
KOVAR, GARY J.

Application No.: 10/085694

Filed: 2/28/2002

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Patent No.: 6905891

Issue Date: 6/14/2005

Examiner: Leonardo Andujar

Group Art Unit: 2826

Docket No.: SC11763TK

Title: METHOD FOR PROCESSING MULTIPLE SEMICONDUCTOR DEVICES FOR TEST

Certificate of Submission

I hereby certify that this correspondence is being submitted to the U.S.P.T.O., Alexandria, VA.

\_\_\_ Addressed per 37 C.F.R. § 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.

\_\_\_ Facsimile transmitted in accordance with 37 C.F.R. § 1.6(d).

☒ Submitted electronically via EFS in accordance with "Legal Framework for EFS Web".

9.8.06  
Date of Submission

Signature

Pat Thomas

Printed Name of Person Signing Certificate

Commissioner for Patents  
Alexandria, VA 22313

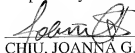
SUBMISSION OF CERTIFICATE OF CORRECTION

Dear Commissioner:

Enclosed is a Certificate of Correction listing error(s) in the subject patent. Please enter these corrections. Since the errors appear to be on the part of the Applicant, please charge the required fee to Deposit Account 503079, Freescale Semiconductor, Inc.

9/08/06  
Date

Respectfully submitted,

  
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**UNITED STATES PATENT AND TRADEMARK OFFICE**  
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PATENT NO.: 6905891  
APPLICATION NO: 10/085694  
DATE: 2/28/2002  
INVENTOR(S): KOVAR, GARY J.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

On the front page, Item (73) Assignee:

Change "Frrescale" to - -Freescale- -.

MAILING ADDRESS OF SENDER (Please do not use customer number below)

Freescale Semiconductor, Inc.  
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Austin, TX 78729